



# OLIFF & BERRIDGE, PLC

ATTORNEYS AT LAW

## Supplemental Application Data Sheet

### Application Information

Application Type::	Regular
Subject Matter::	Utility
CD-ROM or CD-R:	None
Title::	TRANSISTOR, INTEGRATED CIRCUIT, ELECTRO-OPTIC DEVICE, ELECTRONIC INSTRUMENT AND METHOD OF MANUFACTURING A TRANSISTOR
Attorney Docket Number::	117849
Total Drawing Sheets::	10
Small Entity::	No

### Applicant Information

Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
Given Name::	Ichio
Family Name::	YUDASAKA
City of Residence::	<del>Suwa-shi</del> Chino-shi
State or Province of Residence::	Nagano-ken
Country of Residence::	Japan

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Applicant Authority type::	Inventor
Primary Citizenship Country::	Japan
Status::	Full Capacity
Given Name::	Masahiro
Family Name::	FURUSAWA
City of Residence::	<del>Suwa-shi</del> Chino-shi
State or Province of Residence::	Nagano-ken
Country of Residence::	Japan

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Applicant Authority type::	Inventor
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Primary Citizenship Country:: Japan  
 Status:: Full Capacity  
 Given Name:: Takashi  
 Family Name:: AOKI  
 City of Residence:: Suwa-shi  
 State or Province of Residence:: Nagano-ken  
 Country of Residence:: Japan

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**Correspondence Information**

Correspondence Customer Number:: 25944

**Representative Information**

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<b>Foreign Priority Information</b>			
Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-351112	12/03/2002	Yes
<b>Assignee Information</b>			
Assignee Name::		Seiko Epson Corporation	
Street of mailing address::		4-1, Nishishinjuku 2-chome, Shinjuku-ku	
City of mailing address::		Tokyo	
Country of mailing address::		Japan	